

FIG. 1

IMAGE DATA TO BE PROCESSED

- 11 IMAGE DATA INPUT SECTION
- 13 PRINTER ENGINE SECTION
- 21 PAGE DESCRIPTION LANGUAGE PROCESSING SECTION
- 22 IMAGE PROCESSING SECTION
- 24 BAND MEMORY SECTION
- 25 INPUT BUFFER SECTION
- 26 MEMORY TRANSFER SECTION
- 27 SCALING CONTROL SECTION
- 28 BUFFER MEMORY

FIG. 2

- 31 MEMORY CONTROL SECTION
- 32 MAIN SCANNING DIRECTION SCALING SECTION
- 33 MAIN SCANNING DIRECTION SCALING CONTROL SECTION
- 34 SUBSCANNING DIRECTION SCALING CONTROL SECTION
- 35 TRANSFER CONTROL SECTION
- A. RASTER DATA
 - (SCAN ORDER)
- B. DESTINATION ADDRESS, ETC.
- C. PIXEL VALUE INFORMATION

FIG. 3A

$P0,0$	$P0,1$...	$P0,x-1$	$P0,x$...	$P0,W$
$P1,0$	$P1,1$...	$P1,x-1$	$P1,x$...	$P1,W$

FIG. 3B

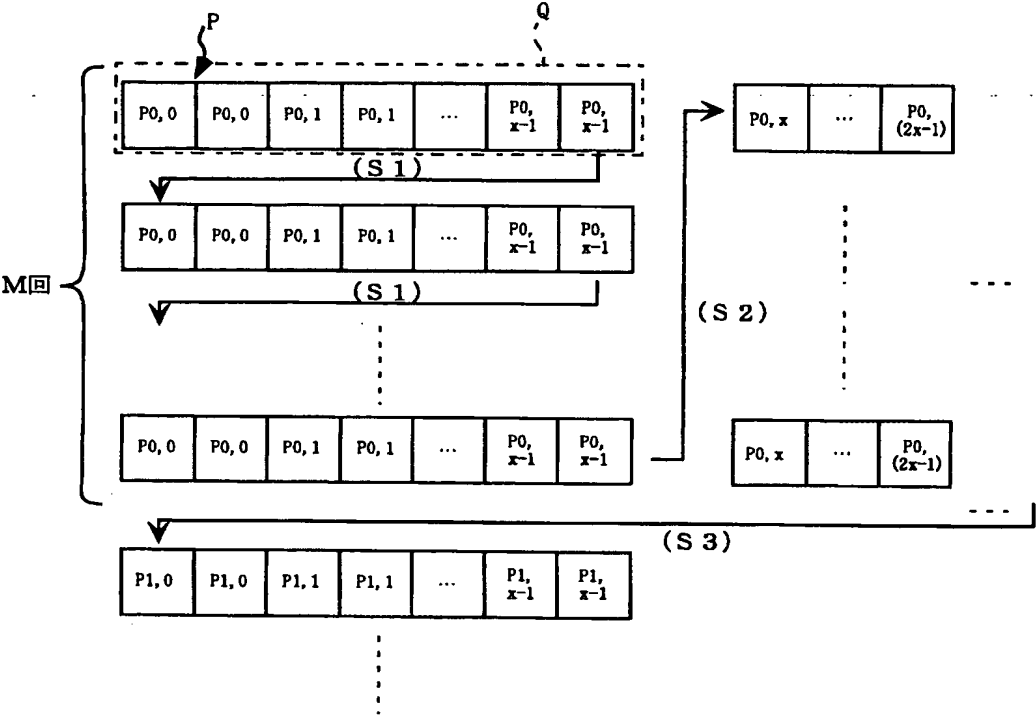


FIG. 3B

M TIMES

FIG. 4

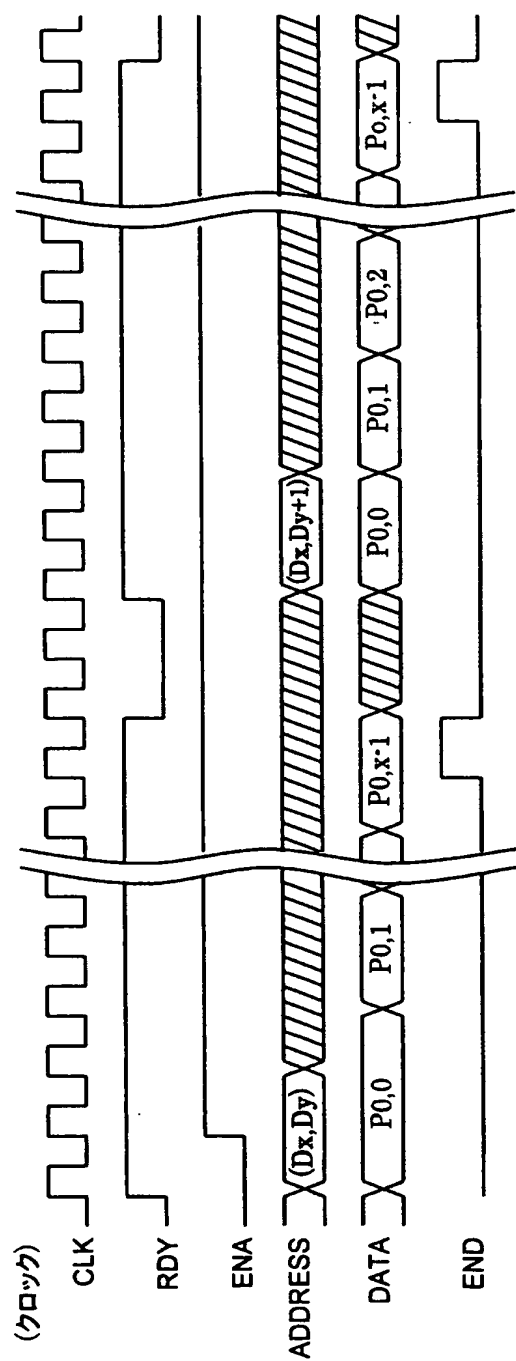


FIG. 4

(CLOCK)

FIG. 5

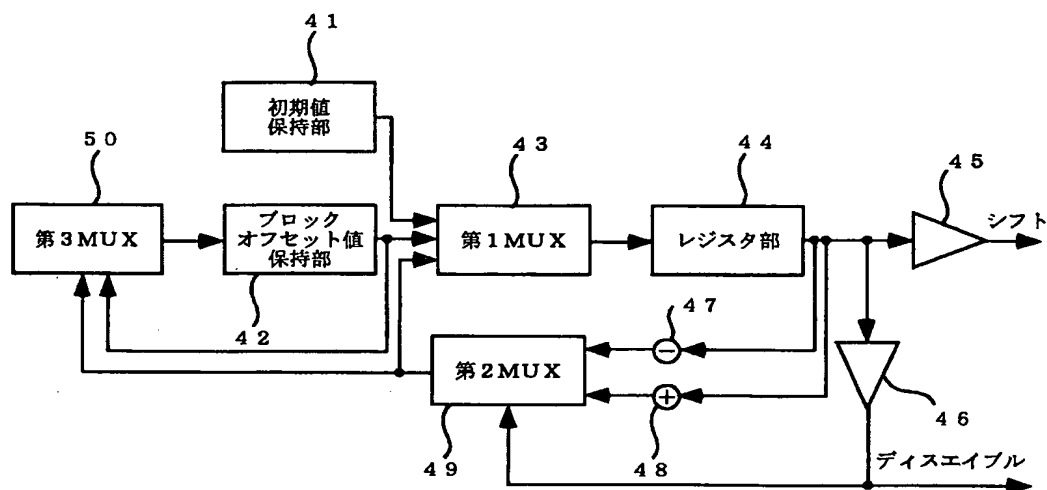


FIG. 6

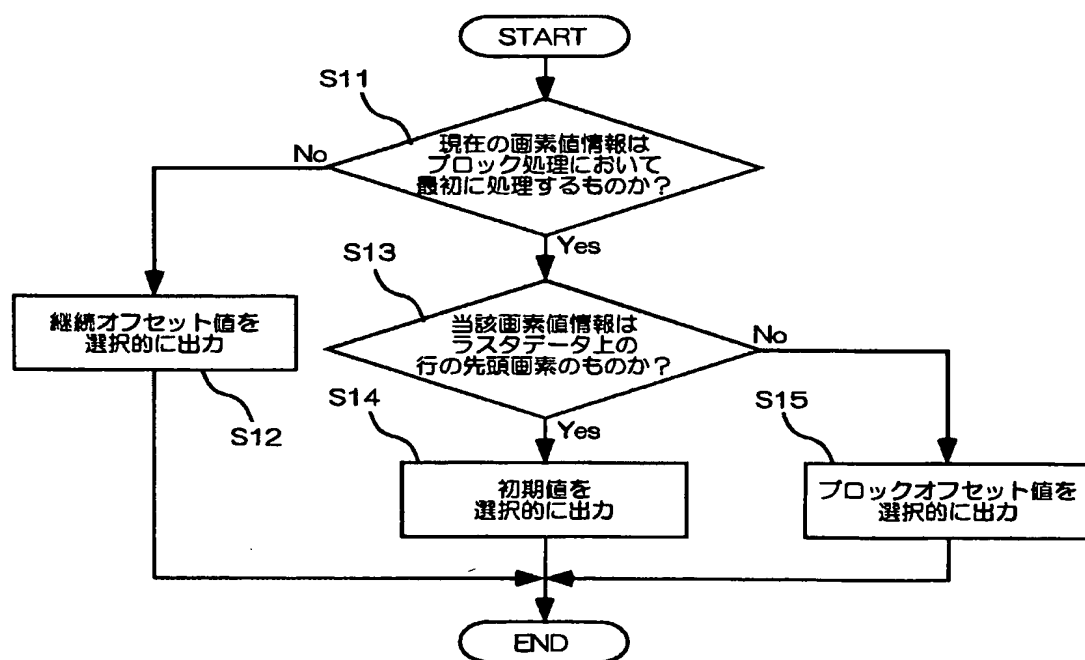


FIG. 5

- 41 INITIAL VALUE RETENTION SECTION
- 42 BLOCK OFFSET VALUE RETENTION SECTION
- 43 FIRST MUX
- 44 REGISTER SECTION
- 49 SECOND MUX
- 50 THIRD MUX
- A. SHIFT
- B. DISABLE

FIG. 6

- S11 CURRENT PIXEL VALUE INFORMATION IS TO BE FIRST PROCESSED
IN BLOCK PROCESSING?
- S12 SELECTIVELY OUTPUT SUCCESSIVE OFFSET VALUE
- S13 PIXEL VALUE INFORMATION IS TOP PIXEL ON RASTER DATA ROW?
- S14 SELECTIVELY OUTPUT INITIAL VALUE
- S15 SELECTIVELY OUTPUT BLOCK OFFSET VALUE

FIG. 7

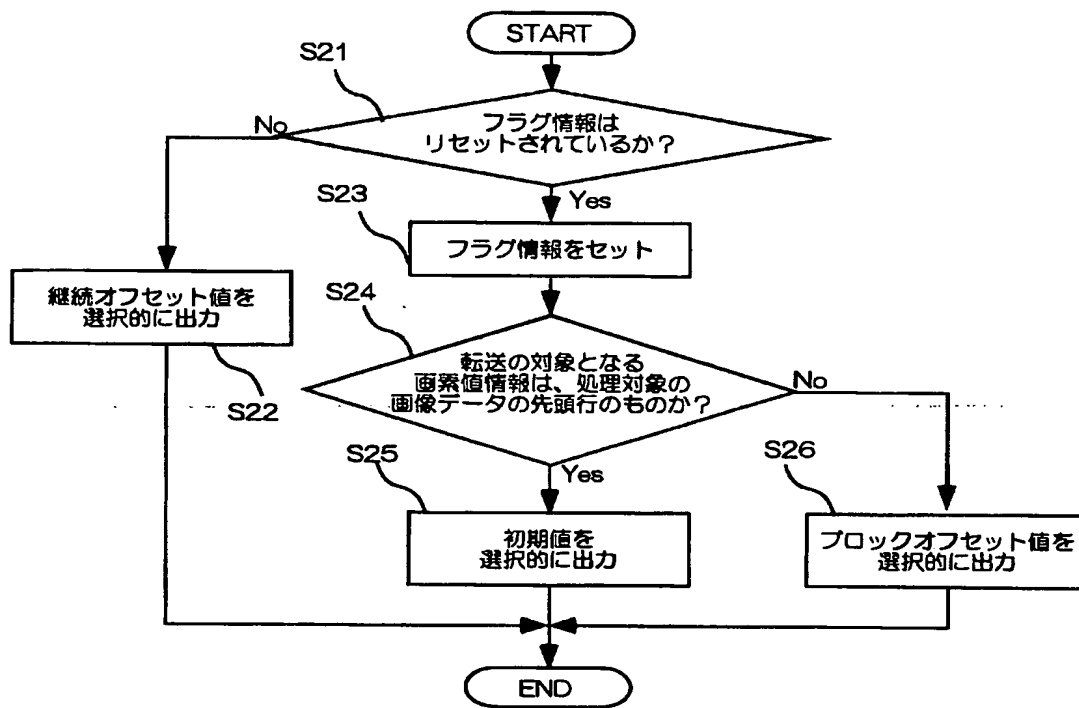


FIG. 7

S21 FLAG INFORMATION RESET?

S22 SELECTIVELY OUTPUT SUCCESSIVE OFFSET VALUE

S23 SET FLAG INFORMATION

S24 PIXEL VALUE INFORMATION TO BE TRANSFERRED IS ON TOP ROW
OF IMAGE DATA TO BE PROCESSED?

S25 SELECTIVELY OUTPUT INITIAL VALUE

S26 SELECTIVELY OUTPUT BLOCK OFFSET VALUE.....

FIG. 8

a		b		c		d		e		f	
クロック	第1選択部が出力する値	デイスエイブル	シフト	転送元アドレス	転送される画素						
1	0. 60	0	0	0	P0, 0						
2	1. 35	0	1	0	P0, 0						
3	1. 10	0	1	1	P0, 1						
4	0. 85	0	0	2	P0, 2						
5	1. 60	0	1	2	P0, 2						
6	1. 35	0	1	3	P0, 3						
7	1. 10	0	1	4	P0, 4						
8	0. 85	0	0	5	P0, 5						
9	1. 60	0	1	5	P0, 5						
.						
.						
.						

FIG. 8

- a CLOCK
- b OUTPUT VALUE OF FIRST SELECTION SECTION
- c DISABLE
- d SHIFT
- e SOURCE ADDRESS
- f TRANSFERRED PIXEL

FIG. 9

a		b		c		d		e		f	
クロック	第1選択部が出力する値	ディスイブル	シフト	転送元アドレス	転送される画素						
1	1. 0	0	1	0	PO, 0						
2	1. 5	0	1	1	PO, 1						
3	2. 0	1	1	2	—						
4	1. 0	0	1	3	PO, 3						
5	1. 5	0	1	4	PO, 4						
6	2. 0	1	1	5	—						
7	1. 0	0	1	6	PO, 6						
8	1. 5	0	1	7	PO, 7						
9	2. 0	1	1	8	—						
.						
.						
.						

FIG. 9

a CLOCK

b OUTPUT VALUE OF FIRST SELECTION SECTION

c DISABLE

d SHIFT

e SOURCE ADDRESS

f TRANSFERRED PIXEL